Sheet 1

MXIC 1571-1

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INFORMATION DISCLOSURE
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Application Number 10/815,370

Filing Date 01 April 2004

First Named Inventor CHIH C, YEH

Art Unit 2827

Examiner Name TRONG Q, PHAN

Attorney Docket Number

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Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (F Anown)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevan Figures Appear
77		^{US-} 6,829,175	12-07-2004	Tsal et al.	
TP		^{US-} 2005/0237809	10-27-2005	Shih et al.	
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		FOREIG	SN PATENT DOCU	MENTS		
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INFORMATION DISCLOSURE	Filing Date	01 APRIL 2004
STATEMENT BY APPLICANT	First Named Inventor	CHIH C. YEH
	Art Unit	2827
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Sheet 2 of 2	Attorney Docket Number	MXIC 1571-1

		NON PATENT LITERATURE DOCUMENTS	,
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
TP		SWIFT, C.T., et al. "An Embedded 90nm SONOS Nonvolatile Memory" IEDM 2002, 8-11 DEC 2002, 927-930.	
77°		SHE, MIN, et. al., "Silicon-Nitride as a Tunnel Dielectric" IEEE Electron Device Letters 24(5), May 2003, 3 pages.	
77°		KIM, BYUNGCHEUL, et al., "A scaled SONOS Single-Transistor" J. Korean Phys. Soc. 41(6), Dec. 2002, 945-948.	

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Sheet 11 of 11	Attorney Docket Number	MXIC 1571-1			

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